

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Not for submission under 37 CFR 1.99)

Application Number	10/538,870
Confirmation Number	8802
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First Named Inventor	Thomas Meredith BROWN
Art Unit	2818
Examiner Name	Thao P. LE
Attorney Docket Number	Q88659

ALL REFERENCES CONSIDERED EXCEPT WHERE LINED THROUGH. /T.L./

U.S. PATENTS

Examiner Initials*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
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U.S. PATENT APPLICATION PUBLICATIONS

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FOREIGN PATENT DOCUMENTS

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	1	0229912	WO	A1	2002-04-11	Cambridge University Technical		Y
	2	0147044	WO	A2	2001-06-28	Plastic Logic Limited		Y
	3	0247183	WO	A1	2002-06-13	Siemens Aktiengesellschaft		Y
	4	0160589	WO	A1	2001-08-23	Omilion Technologies LLC.		Y

NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city, and/or country where published.	T ⁵
	1	Francis GARNIER, et al., VERTICAL DEVICE ARCHITECTURE BY MOLDING OF ORGANIC-BASED THIN FILM TRANSISTOR, Applied Physics Letters, Volume 73, Number 12, September 21, 1998, pp. 1721-1723	Y
	2	H. Sirringhaus, et al., HIGH-RESOLUTION INK-JET PRINTING OF ALL-POLYMER TRANSISTOR CIRCUITS, MRS BULLETIN/July 2001, pp. 539-543	Y
	3	C.R. KAGAN, et al., PATTERNING ORGANIC-INORGANIC THIN-FILM TRANSISTORS USING MICROCONTACT PRINTED TEMPLATES, Applied Physics Letters, Volume 79, Number 21, November 19, 2001 pp. 3536-3538.	Y

EXAMINER SIGNATURE

Examiner Signature	/Thao Le/	Date Considered	09/22/2008
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through a citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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